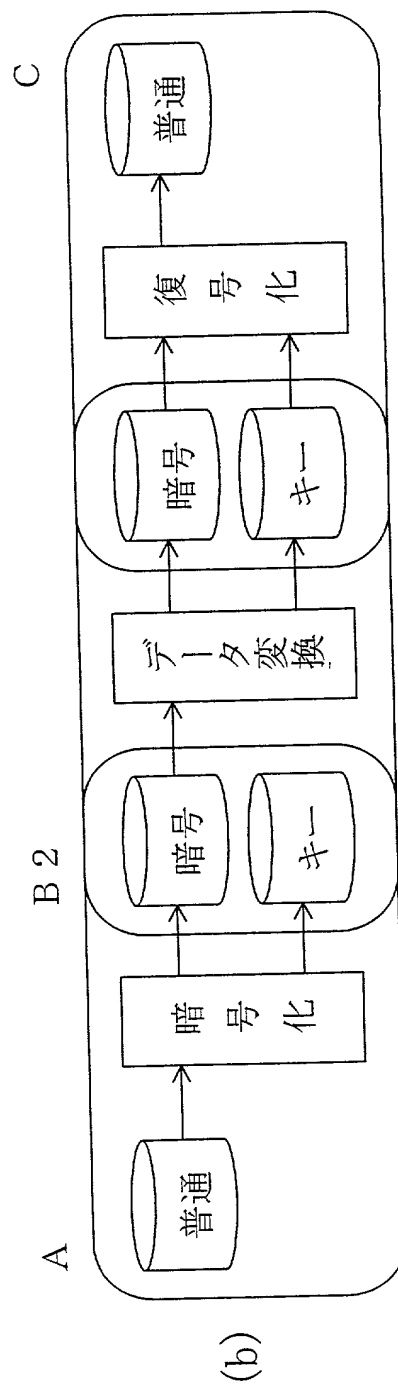
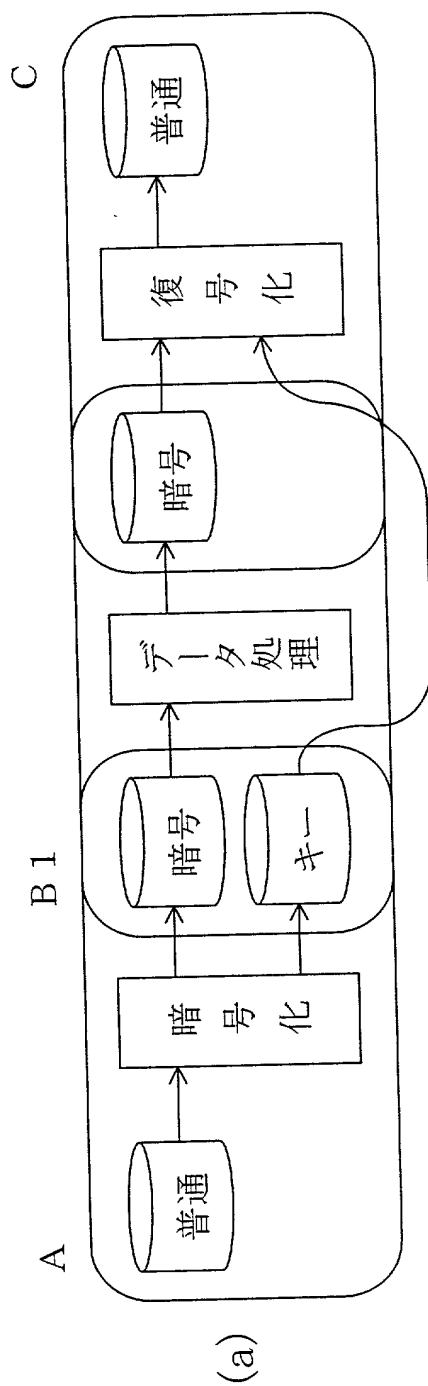
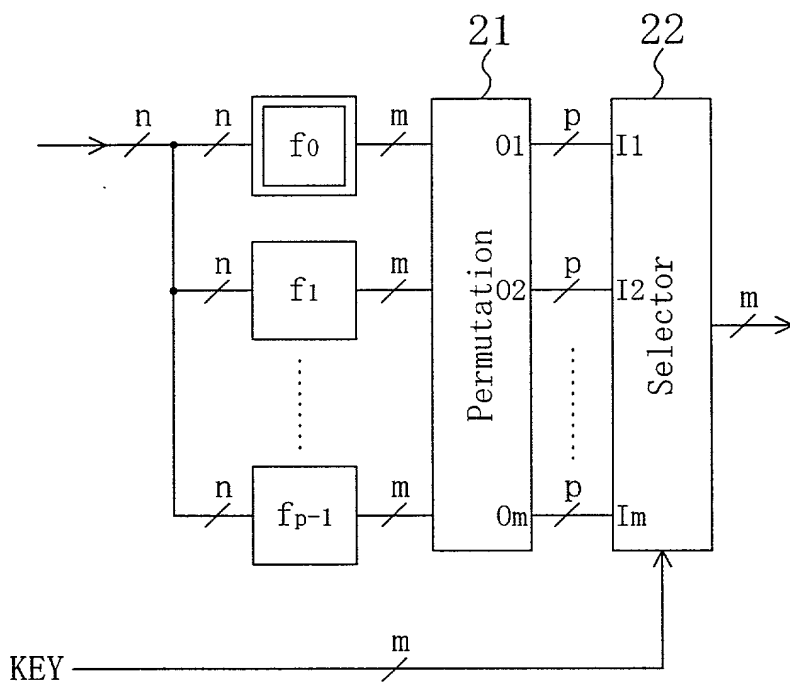
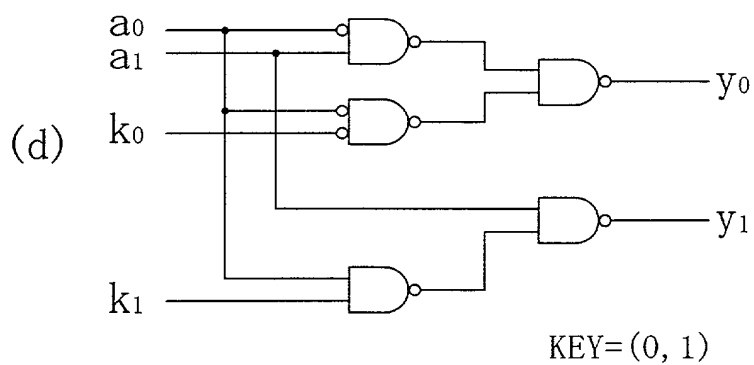
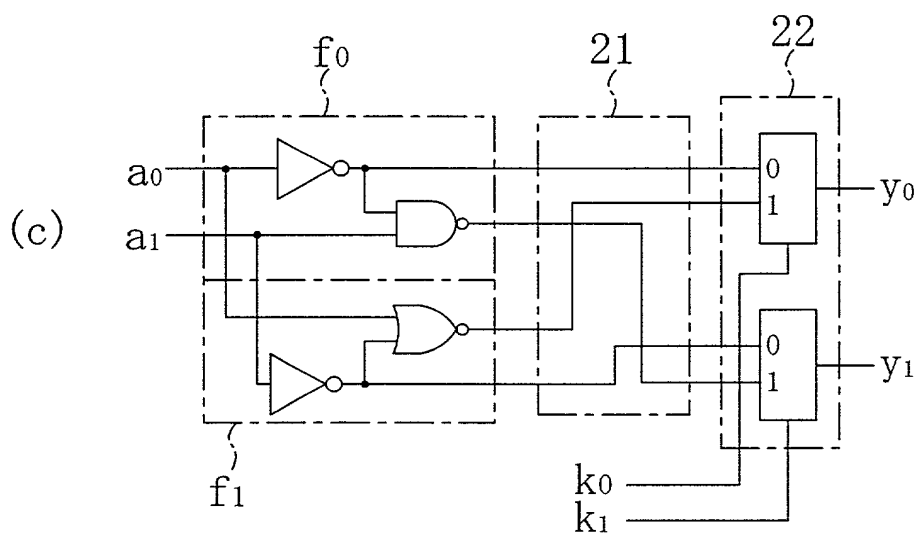
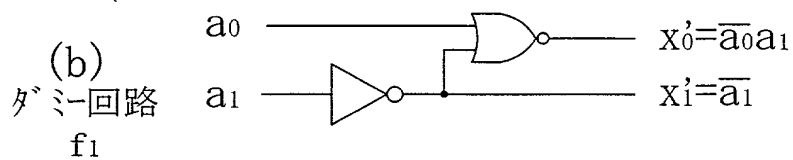
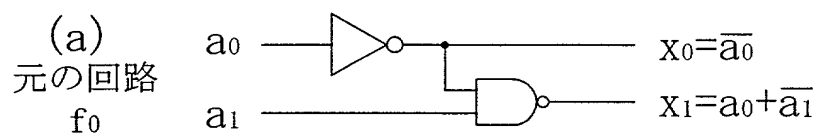
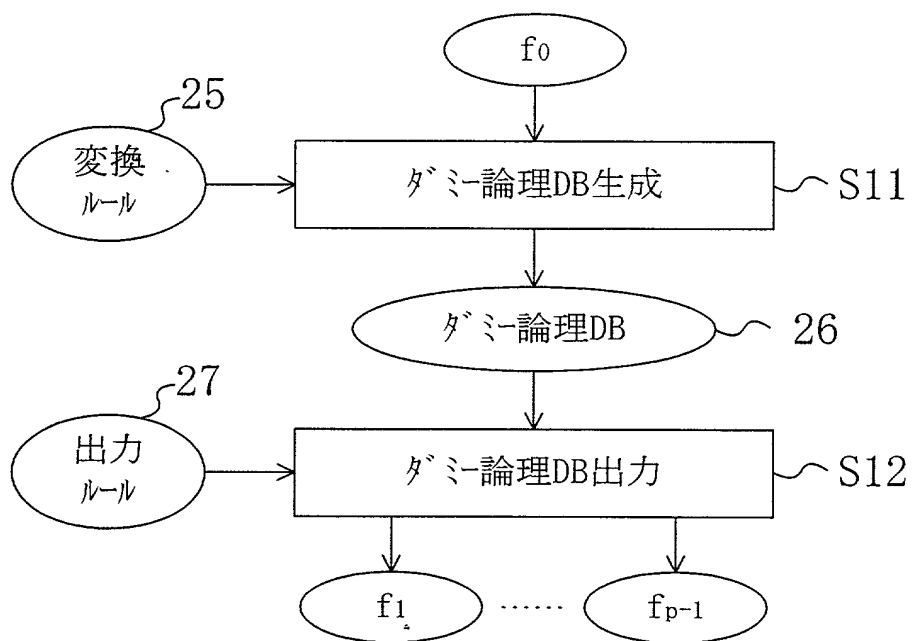


Figure 3 shows two diagrams, (a) and (b), illustrating a data processing flow. Both diagrams are divided into three main sections: A, B, and C. Section A contains a '普通' (Ordinary) cylinder. Section B contains a '暗号化' (Encryption) block and a 'データ処理' (Data Processing) block. Section C contains a '復号化' (Decryption) block and a '普通' (Ordinary) cylinder. In diagram (a), the flow is: A (普通) → B (暗号化) → B (データ処理) → C (復号化) → C (普通). In diagram (b), the flow is: A (普通) → B (暗号化) → B (データ変換) → C (復号化) → C (普通). Both diagrams also show a 'キー' (Key) cylinder in section B, which is used in the encryption and decryption processes.

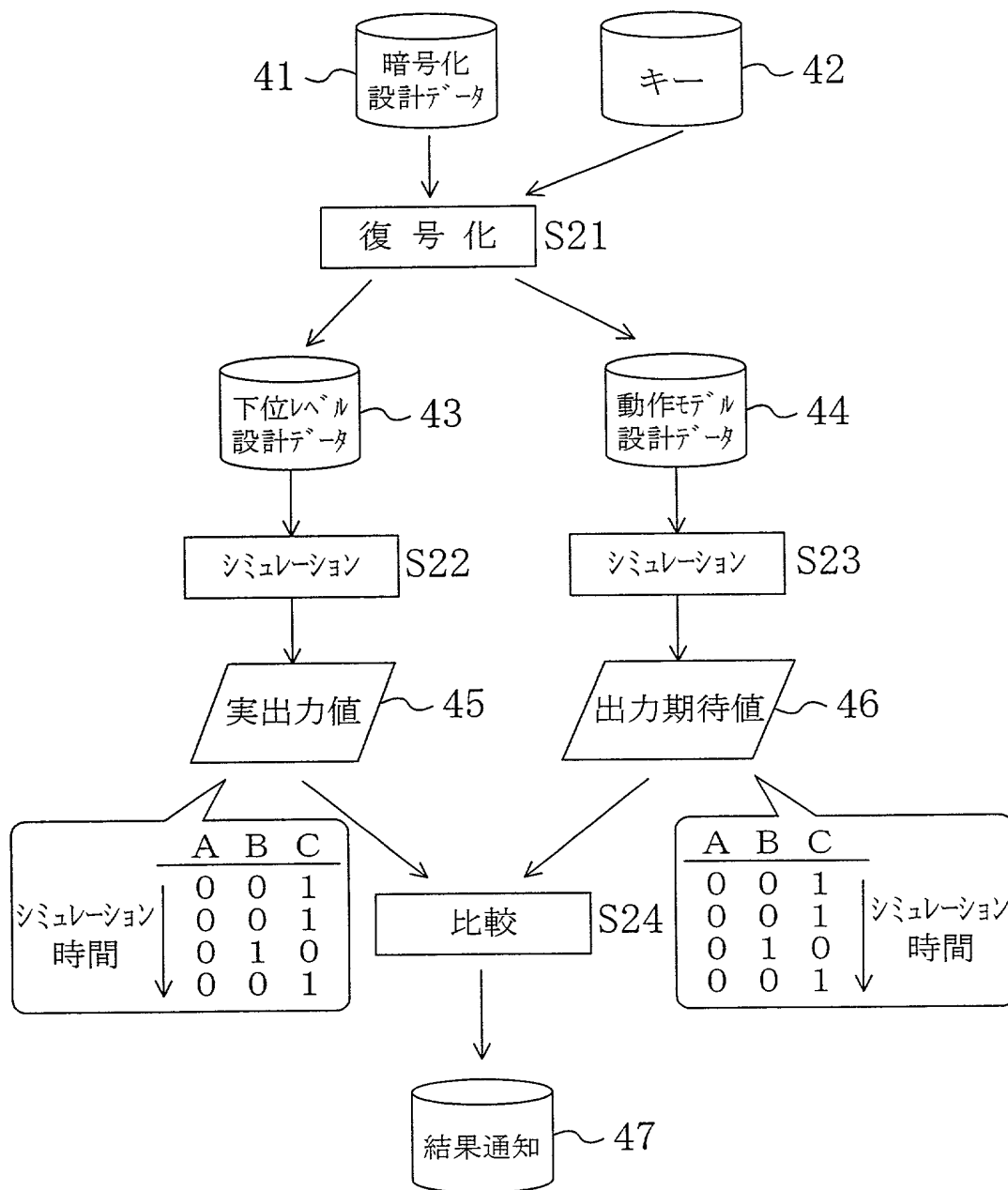


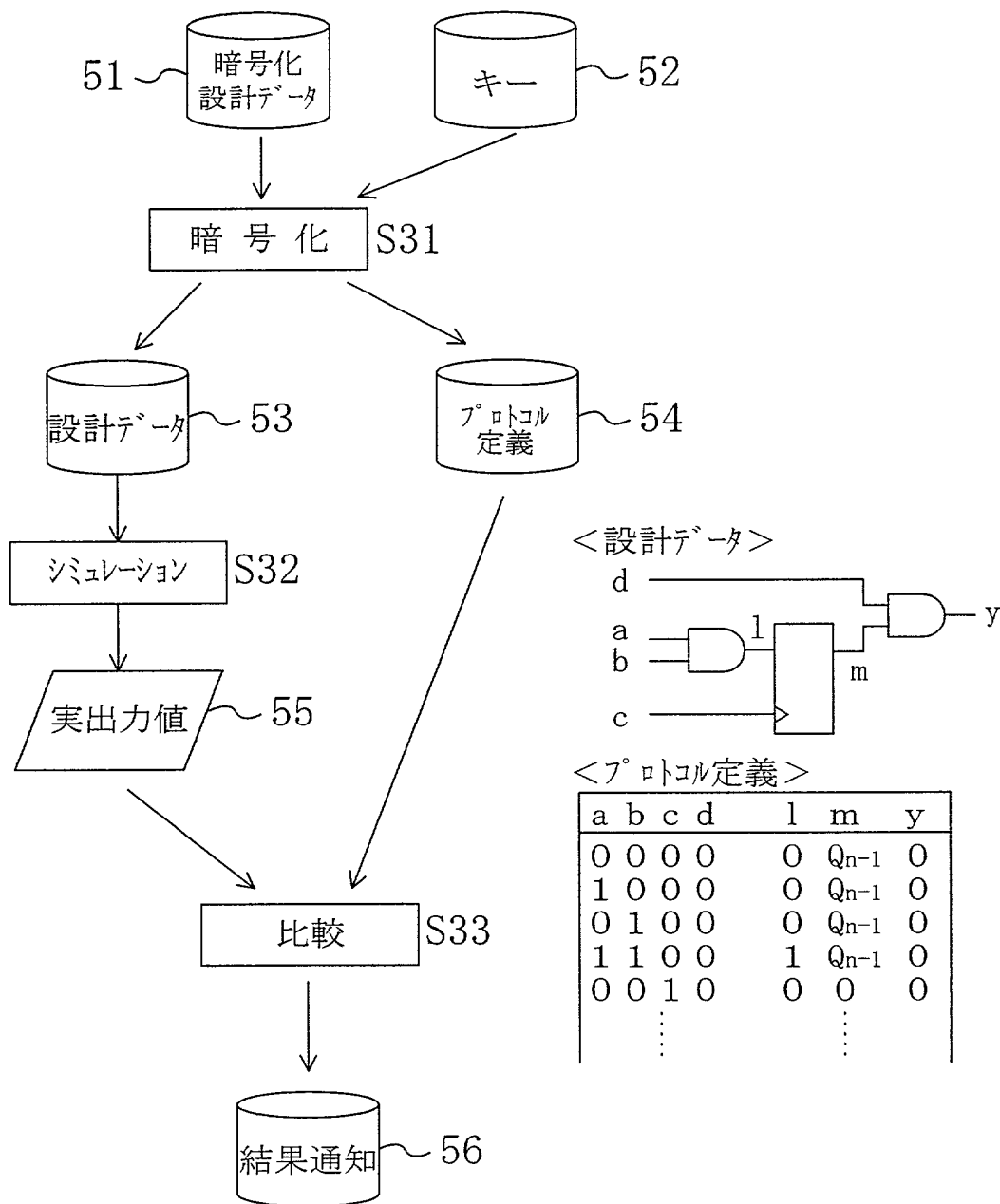


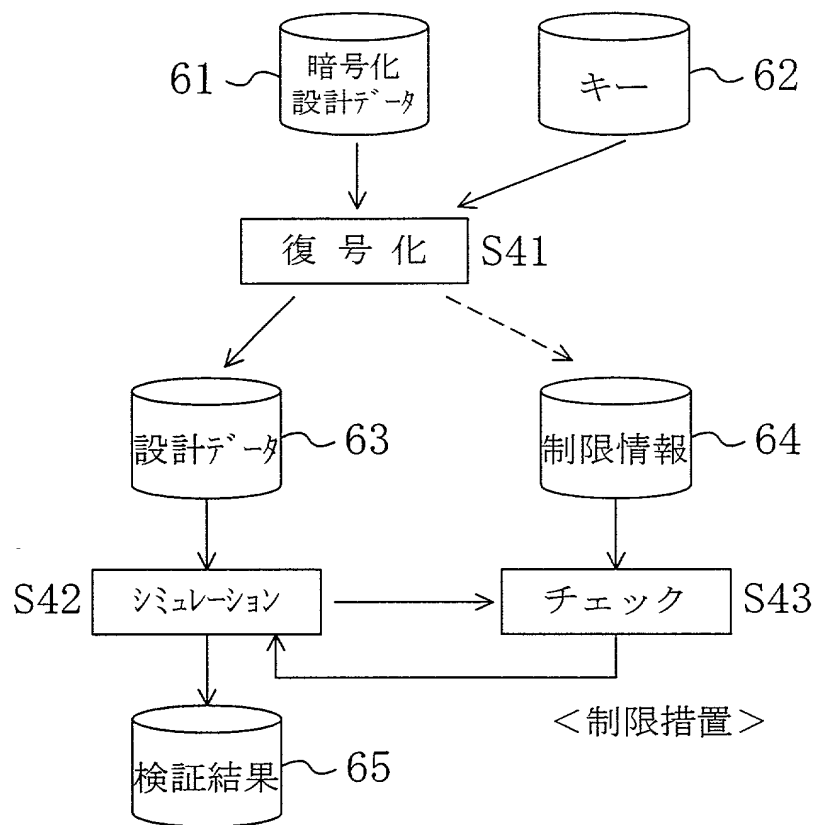


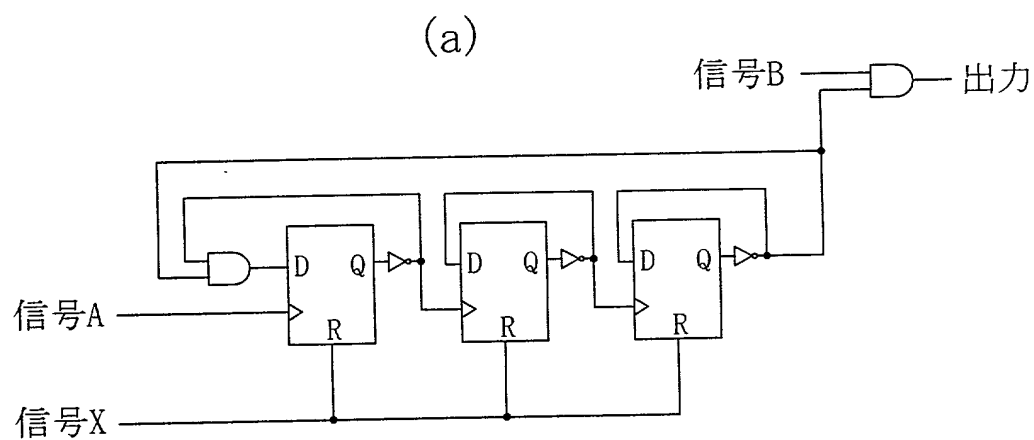






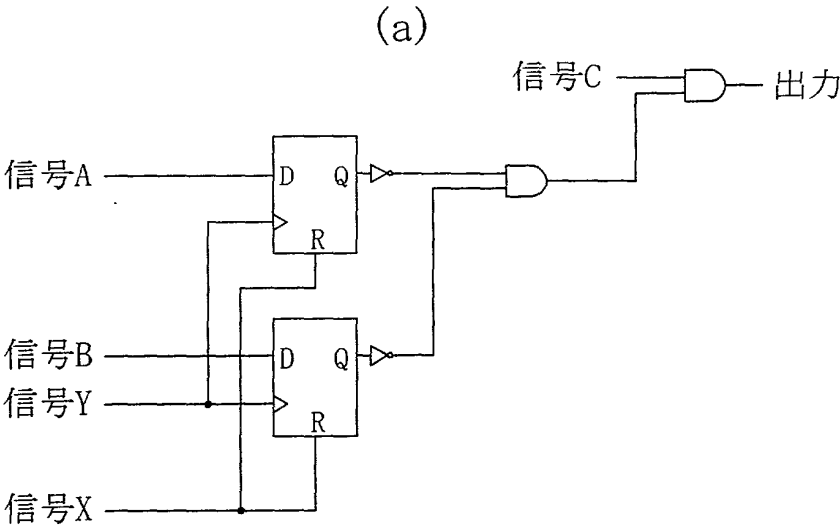






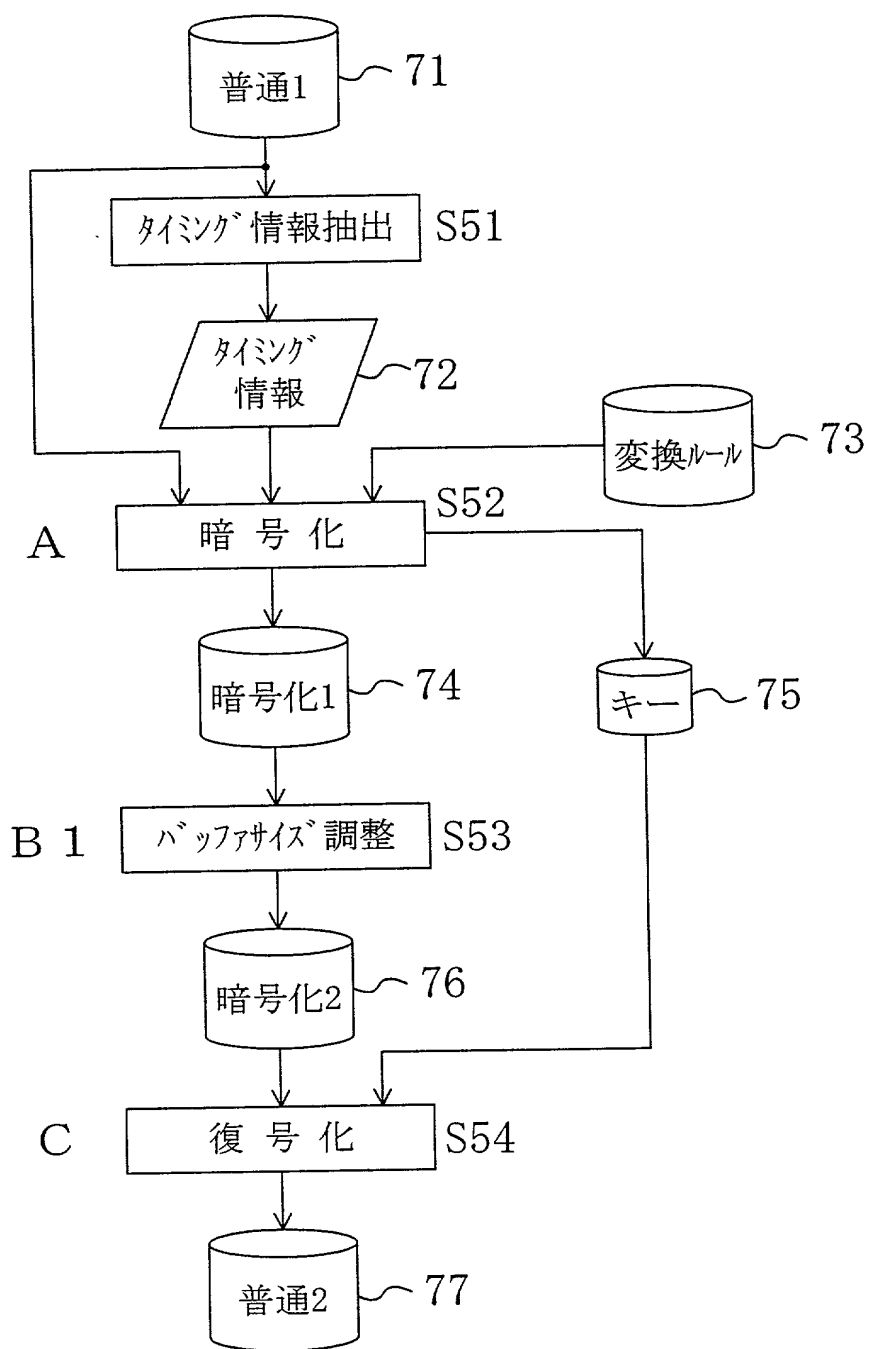
(b)

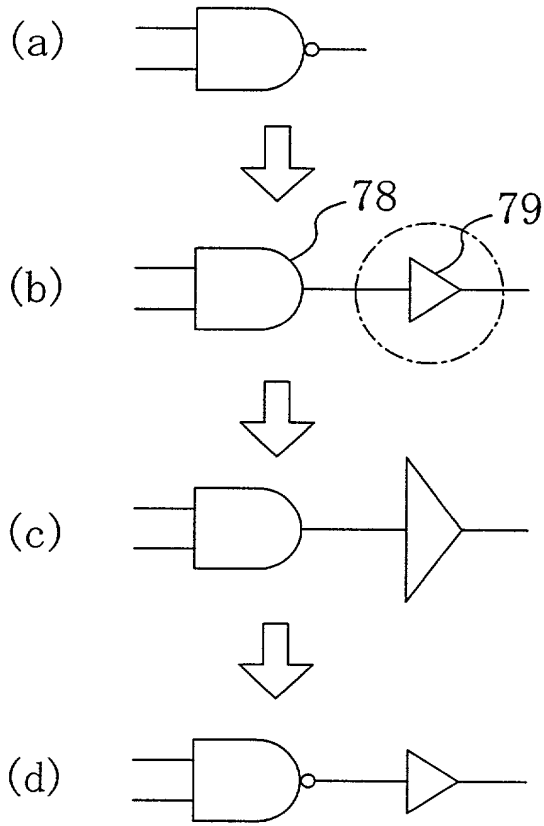
X	Aの変化回数	B	出力
0	1	0	0
		1	1
			} OK
0	2	0	0
		1	1
			} OK
	⋮		⋮
0	7	0	0
		1	1
			} OK
<hr style="border-top: 1px dashed black;"/>			
0	8	0	0
		1	0
			} NG
0	9	0	0
		1	0
			} NG
	⋮		⋮



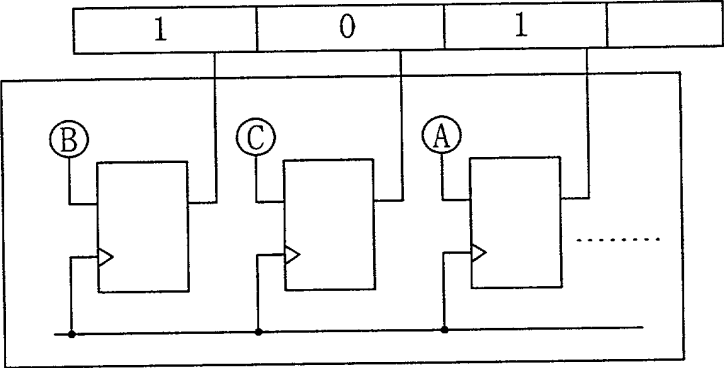
(b)

X	Y	A	B	C	出力	
0	↑	0	0	0	0	} OK
		0	0	1	1	
<hr/>						
		0	1	0	0	} NG
		0	1	1	0	
		1	0	0	0	
		1	0	1	0	
		1	1	0	0	
		1	1	1	0	





回路固有IDレジスタ



$$\left. \begin{array}{l} A = 1 \\ B = 1 \\ C = 0 \end{array} \right\} \text{固有パラメータ}$$

